

Day : Wednesday

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**Inventor Name Search Result**

Your Search was:

Last Name = KNIFFLER

First Name = OLIVER

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>09928011</u>	Not Issued	41	08/10/2001	Microprocessor and method of addressing in a microprocessor	KNIFFLER, OLIVER
<u>09953725</u>	<u>6543019</u>	150	09/17/2001	METHOD FOR BUILT-IN SELF TEST OF AN ELECTRONIC CIRCUIT	KNIFFLER, OLIVER
<u>10112290</u>	Not Issued	41	03/28/2002	Method for operating a processor bus	KNIFFLER, OLIVER
<u>10160967</u>	<u>7036017</u>	150	06/03/2002	MICROPROCESSOR CONFIGURATION WITH ENCRYPTION	KNIFFLER, OLIVER
<u>10197789</u>	Not Issued	93	07/18/2002	MICROPROCESSOR CONFIGURATION WITH ENCRYPTION	KNIFFLER, OLIVER
<u>10197792</u>	Not Issued	71	07/18/2002	Microprocessor configuration and method for operating a microprocessor configuration	KNIFFLER, OLIVER
<u>10199589</u>	<u>7020730</u>	150	07/19/2002	METHOD FOR OPERATING A MICROPROCESSOR CONFIGURATION AND MICROPROCESSOR CONFIGURATION	KNIFFLER, OLIVER
<u>10418991</u>	<u>7181632</u>	150	04/18/2003	DATA PROCESSING APPARATUS AND METHOD FOR OPERATING A DATA PROCESSING MODULE FOR SECURE POWER SAVING	KNIFFLER, OLIVER
<u>10662794</u>	Not Issued	30	09/15/2003	Data bus configuration having a data bus which can be operated in multiplex mode, and method for operating the configuration	KNIFFLER, OLIVER
<u>10701058</u>	Not Issued	61	11/04/2003	Frequency regulating circuit	KNIFFLER, OLIVER
<u>10893161</u>	<u>7178168</u>	150	07/16/2004	SHIFT DEVICE AND METHOD	KNIFFLER,

				FOR SHIFTING	OLIVER
<u>10893163</u>	Not Issued	20	07/16/2004	Calculating unit and method for performing an arithmetic operation with encrypted operands	KNIFFLER, OLIVER
<u>11132145</u>	Not Issued	71	05/20/2005	Controller having an instruction set	KNIFFLER, OLIVER
<u>11134612</u>	Not Issued	41	05/20/2005	Controller and method for processing instructions	KNIFFLER, OLIVER
<u>11383143</u>	Not Issued	30	05/12/2006	Device and method for determining a position of a bit error in a bit sequence	KNIFFLER, OLIVER
<u>11444832</u>	Not Issued	25	06/01/2006	Communication device and method of transmitting data	KNIFFLER, OLIVER
<u>11609246</u>	Not Issued	30	12/11/2006	DATA PROCESSING SYSTEM AND METHOD FOR OPERATING A DATA PROCESSING SYSTEM	KNIFFLER, OLIVER
<u>11673255</u>	Not Issued	20	02/09/2007	DATA-PROCESSING APPARATUS AND METHOD FOR PROCESSING DATA	KNIFFLER, OLIVER
<u>11695515</u>	Not Issued	19	04/02/2007	HEAT PUMP	KNIFFLER, OLIVER
<u>60789324</u>	Not Issued	159	04/04/2006	Waermepumpe	KNIFFLER, OLIVER

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**Inventor Name Search Result**

Your Search was:

Last Name = SEDLAK

First Name = HOLGER

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>07022970</u>	4870681	150	03/05/1987	CRYPTOGRAPHIC METHOD AND CRYPTOGRAPHIC PROCESSOR FOR CARRYING OUT THE METHOD	SEDLAK, HOLGER
<u>08858819</u>	5981342	250	05/19/1997	THE METHOD OF MAKING A SEMICONDUCTOR COMPONENT WITH COMPENSATION IMPLANTATION	SEDLAK, HOLGER
<u>08907211</u>	5963478	150	08/06/1997	EEPROM AND METHOD OF DRIVING THE SAME	SEDLAK, HOLGER
<u>08907212</u>	5999035	150	08/06/1997	METHOD FOR DRIVING A FIELD-EFFECT TRANSISTOR	SEDLAK, HOLGER
<u>09068430</u>	6052306	150	05/08/1998	METHOD AND DEVICE FOR AUTOMATIC DETERMINATION OF THE REQUIRED HIGH VOLTAGE FOR PROGRAMMING/ERASING AN EEPROM	SEDLAK, HOLGER
<u>09091152</u>	6160295	150	09/02/1998	CMOS DEVICE	SEDLAK, HOLGER
<u>09093573</u>	6166952	150	06/08/1998	READ ONLY MEMORY HAVING SPECIALLY DESIGNED OUTPUT CIRCUITS AND GROUPS OF MEMORY CELLS EACH CONNECTED TO A RESPECTIVE WORD LINE	SEDLAK, HOLGER
<u>09106236</u>	Not Issued	161	06/29/1998	Method and apparatus for the encryption /decryption of data where an encryption/decryption step is randomly selected and/or altered	SEDLAK, HOLGER
<u>09142753</u>	6059191	150	09/14/1998	SMART CARD	SEDLAK, HOLGER

<u>09163627</u>	<u>5991207</u>	150	09/28/1998	CIRCUIT CONFIGURATION HAVING A NUMBER OF ELECTRONIC CIRCUIT COMPONENTS	SEDLAK, HOLGER
<u>09171481</u>	<u>6044021</u>	150	10/19/1998	PROGRAMMABLE READ-ONLY MEMORY WITH IMPROVED ACCESS TIME	SEDLAK, HOLGER
<u>09390493</u>	<u>6182217</u>	150	09/03/1999	ELECTRONIC DATA- PROCESSING DEVICE AND SYSTEM	SEDLAK, HOLGER
<u>09395998</u>	<u>6487649</u>	150	09/13/1999	MICROCOMPUTER WHICH ALLOWS ACCESS TO LIBRARY PROGRAMS	SEDLAK, HOLGER
<u>09401385</u>	<u>6376875</u>	150	09/22/1999	SEMICONDUCTOR COMPONENT WITH COMPENSATION IMPLANTATION	SEDLAK, HOLGER
<u>09483734</u>	<u>6266274</u>	150	01/14/2000	Semiconductor Memory With Non- Volatile Two-Transistor Memory Cells	SEDLAK, HOLGER
<u>09499566</u>	<u>6510514</u>	150	02/07/2000	DEVICE FOR RELIABILITY CREATING ELECTRONIC SIGNATURES	SEDLAK, HOLGER
<u>09500415</u>	<u>6615351</u>	150	02/08/2000	METHOD FOR CHECKING THE AUTHENTICITY OF A DATA MEDIUM	SEDLAK, HOLGER
<u>09771886</u>	<u>6864730</u>	150	01/29/2001	CLOCKED INTEGRATED SEMICONDUCTOR CIRCUIT AND METHOD FOR OPERATING SUCH A CIRCUIT	SEDLAK, HOLGER
<u>09771914</u>	<u>6581842</u>	150	01/29/2001	DATA CARRIER WITH REGULATION OF THE POWER CONSUMPTION	SEDLAK, HOLGER
<u>09783183</u>	Not Issued	30	02/14/2001	Read-only memory having specially output circuits and word line connected to a group of memory cells	SEDLAK, HOLGER
<u>09928011</u>	Not Issued	41	08/10/2001	Microprocessor and method of addressing in a microprocessor	SEDLAK, HOLGER
<u>10112290</u>	Not Issued	41	03/28/2002	Method for operating a processor bus	SEDLAK, HOLGER
<u>10160967</u>	<u>7036017</u>	150	06/03/2002	MICROPROCESSOR CONFIGURATION WITH ENCRYPTION	SEDLAK, HOLGER
<u>10197789</u>	Not	93	07/18/2002	MICROPROCESSOR	SEDLAK,



	Issued			CONFIGURATION WITH ENCRYPTION	HOLGER
<u>10197792</u>	Not Issued	71	07/18/2002	Microprocessor configuration and method for operating a microprocessor configuration	SEDLAK, HOLGER
<u>10360454</u>	Not Issued	61	02/06/2003	Method and apparatus for processing data where a part of the current supplied is supplied to an auxiliary circuit	SEDLAK, HOLGER
<u>10461905</u>	Not Issued	61	06/13/2003	Cryptography processor	SEDLAK, HOLGER
<u>10461913</u>	Not Issued	61	06/13/2003	Cryptographic processor	SEDLAK, HOLGER
<u>10480081</u>	<u>7124275</u>	150	08/17/2004	APPARATUS AND METHOD FOR DETERMINING A PHYSICAL ADDRESS FROM A VIRTUAL ADDRESS BY USING A HIERARCHICAL MAPPING REGULATION WITH COMPRESSED NODES	SEDLAK, HOLGER
<u>10621536</u>	Not Issued	41	07/17/2003	Method for increasing the security of a CPU	SEDLAK, HOLGER
<u>10622981</u>	Not Issued	93	07/18/2003	MICROPROCESSOR CIRCUIT FOR PORTABLE DATA CARRIERS AND METHOD FOR OPERATING THE CIRCUIT	SEDLAK, HOLGER
<u>10623830</u>	<u>6920473</u>	150	07/21/2003	METHOD AND APPARATUS FOR MODULAR MULTIPLYING AND CALCULATING UNIT FOR MODULAR MULTIPLYING	SEDLAK, HOLGER
<u>10635599</u>	Not Issued	93	08/06/2003	MICROPROCESSOR CIRCUIT FOR DATA CARRIERS AND METHOD FOR ORGANIZING ACCESS TO DATA STORED IN A MEMORY	SEDLAK, HOLGER
<u>10653536</u>	<u>6839287</u>	150	09/02/2003	METHOD OF STORING A QUANTITY OF DATA IN A TARGET MEMORY LOCATION AND STORAGE SYSTEM	SEDLAK, HOLGER
<u>10662627</u>	<u>7120660</u>	150	09/15/2003	METHOD OF AND APPARATUS FOR MODULAR MULTIPLICATION	SEDLAK, HOLGER
<u>10662794</u>	Not Issued	30	09/15/2003	Data bus configuration having a data bus which can be operated in multiplex mode, and method for operating the configuration	SEDLAK, HOLGER

<u>10667567</u>	Not Issued	41	09/22/2003	Data carrier	SEDLAK, HOLGER
<u>10694591</u>	Not Issued	161	10/27/2003	Method for identifying a correct command entry address when using command words of different length	SEDLAK, HOLGER
<u>10701058</u>	Not Issued	61	11/04/2003	Frequency regulating circuit	SEDLAK, HOLGER
<u>10723432</u>	Not Issued	71	11/25/2003	Electronic circuit with asynchronous clocking of peripheral units	SEDLAK, HOLGER
<u>10724016</u>	Not Issued	41	11/25/2003	Electronic circuit with energy control	SEDLAK, HOLGER
<u>10768988</u>	6940755	150	01/30/2004	SEMICONDUCTOR WITH AN IMPROVED READ DEVICE AND OPERATIONAL MODE ASSOCIATED THEREWITH	SEDLAK, HOLGER
<u>10789462</u>	7016927	150	02/26/2004	METHOD AND APPARATUS FOR MODULAR MULTIPLICATION	SEDLAK, HOLGER
<u>10854928</u>	7167388	150	05/26/2004	INTEGRATED CIRCUIT AND METHOD FOR OPERATING AN INTEGRATED CIRCUIT	SEDLAK, HOLGER
<u>10857617</u>	7181576	150	05/27/2004	METHOD FOR SYNCHRONIZING A CACHE MEMORY WITH A MAIN MEMORY	SEDLAK, HOLGER
<u>10864822</u>	Not Issued	25	06/08/2004	Data processing device	SEDLAK, HOLGER
<u>10871523</u>	7185245	150	06/17/2004	TEST READING APPARATUS FOR MEMORIES	SEDLAK, HOLGER
<u>10913956</u>	Not Issued	25	08/06/2004	Key management device and method for the encrypted storage of digital data words	SEDLAK, HOLGER
<u>10934301</u>	6999337	150	09/03/2004	REGISTER CELL AND METHOD FOR WRITING TO THE REGISTER CELL	SEDLAK, HOLGER
<u>10943701</u>	6977831	150	09/17/2004	CONTENT ADDRESSABLE MEMORY CELL	SEDLAK, HOLGER

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**Inventor Name Search Result**

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Last Name = SEDLAK

First Name = HOLGER

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>10957536</u>	6970899	150	10/01/2004	CALCULATING UNIT AND METHOD FOR SUBTRACTING	SEDLAK, HOLGER
<u>10959907</u>	6965910	150	10/06/2004	CALCULATING UNIT AND METHOD FOR ADDING	SEDLAK, HOLGER
<u>10963426</u>	6985917	150	10/11/2004	CONFIGURABLE CALCULATING UNIT	SEDLAK, HOLGER
<u>10965663</u>	Not Issued	30	10/14/2004	Circuit arrangement and method for producing a dual-rail signal	SEDLAK, HOLGER
<u>10977561</u>	Not Issued	30	10/28/2004	Apparatus and method for calculating a result of a modular multiplication	SEDLAK, HOLGER
<u>11004658</u>	6970016	150	12/03/2004	DATA PROCESSING CIRCUIT AND METHOD FOR TRANSMITTING DATA	SEDLAK, HOLGER
<u>11006519</u>	Not Issued	30	12/06/2004	Processor and method for a simultaneous execution of a calculation and a copying process	SEDLAK, HOLGER
<u>11008585</u>	Not Issued	30	12/09/2004	Device and method for generating random numbers using a pseudo random number generator	SEDLAK, HOLGER
<u>11017209</u>	Not Issued	80	12/20/2004	Apparatus and method for processing a sequence of jump instructions	SEDLAK, HOLGER
<u>11080998</u>	Not Issued	30	03/14/2005	Calculating unit	SEDLAK, HOLGER
<u>11090914</u>	Not Issued	30	03/24/2005	Apparatus and method for converting, and adder circuit	SEDLAK, HOLGER
<u>11125819</u>	Not Issued	30	05/09/2005	Memory arrangement	SEDLAK, HOLGER
<u>11165834</u>	Not	30	06/23/2005	Modular multiplication with	SEDLAK, HOLGER

	Issued			parallel calculation of the look-ahead parameters	
<u>11166645</u>	Not Issued	30	06/23/2005	Apparatus and method for calculating a multiplication	SEDLAK, HOLGER
<u>11305821</u>	Not Issued	30	12/16/2005	Circuit arrangement	SEDLAK, HOLGER
<u>11340000</u>	Not Issued	41	01/25/2006	Circuit arrangement for voltage adjustment	SEDLAK, HOLGER
<u>11440725</u>	Not Issued	20	05/25/2006	Method for modular multiplication	SEDLAK, HOLGER
<u>11555799</u>	Not Issued	30	11/02/2006	MEMORY CIRCUIT AND METHOD FOR WRITING INTO A TARGET MEMORY AREA	SEDLAK, HOLGER
<u>11563062</u>	Not Issued	30	11/24/2006	FLIP-FLOP DEVICE AND METHOD FOR STORING AND OUTPUTTING A DATA VALUE	SEDLAK, HOLGER
<u>11564695</u>	Not Issued	20	11/29/2006	DEVICE AND METHOD FOR NON-VOLATILE STORAGE OF A STATUS VALUE	SEDLAK, HOLGER
<u>11615542</u>	Not Issued	25	12/22/2006	CIRCUIT ARRANGEMENT AND METHOD FOR OPERATING A CIRCUIT ARRANGEMENT	SEDLAK, HOLGER
<u>11695515</u>	Not Issued	19	04/02/2007	HEAT PUMP	SEDLAK, HOLGER
<u>60789324</u>	Not Issued	159	04/04/2006	Waermepumpe	SEDLAK, HOLGER

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